JETTA

Special Issue on Plug-and-Play Test Automation for System-on-a-Chip

Journal of Electronic Testing: Theory and Applications

Editor-in-Chief: Vishwani D. Agrawal

Special Issue Guest Editor: Krishnendu Chakrabarty

http://www.wkap.nl/journalhome.htm/0923-8174

System-on-a-chip (SOC) design and test engineers need plug-and-play test automation techniques in order to reduce test cost and meet time-to-market. The purpose of this special issue of JETTA is to bring together survey papers, tutorials, and research articles in a single issue of the journal. It will contain papers on all aspects of core-based SOC testing. Papers related to plug-and-play test automation are especially welcome. The topics of interest include, but are not limited to:

- Test access mechanisms and test scheduling
- Testing using embedded microprocessors
- Test generation for crosstalk-induced faults
- BIST techniques, test data compression, and on-chip decompression
- Hierarchical test and transparency-based testing
- Testing of mixed-signal and analog cores

Authors should submit previously unpublished papers by e-mail (postscript or pdf format) to the guest editor, and six hard-copies to the JETTA editorial office, clearly mentioning that the submission is for the special issue on SOC. Expanded versions of conference papers are also welcome. The final selections will be made through the journal's peer review process. The schedule is as follows:

- Paper submission deadline: September 1, 2001
- Acceptance/revision/rejection notification: December 15, 2001
- Final manuscript: February 1, 2002
- Tentative publication date: August 2002.

GUEST EDITOR

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Instructions for authors may be found on the inside back cover of the journal, or at the back of this call for papers.

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INSTRUCTIONS FOR AUTHORS

Authors are encouraged to submit high quality, original work that has not appeared in, nor is under consideration by, other journals. Papers which have previously appeared in conference proceedings will be considered, and this should be so indicated at the time of submission.

PROCESS FOR SUBMISSION

1. Authors should submit *six* copies of their final manuscript to:

   Bonny L. Blaisdell
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   For prompt attention, all correspondence can be directed to this address.

2. Enclose originals for the illustrations, in the style described below, for one copy of the manuscript. Photocopies of the figures may accompany the remaining copies of the manuscript. Alternatively, original illustrations may be submitted after the paper has been accepted.

3. The title should show names, addresses and email addresses of all authors as they will appear in the paper. Enclose a separate page giving the preferred address of the contact author for correspondence and return of proofs. Please include telephone number, fax number and email address.

4. The refereeing is done by anonymous reviewers under the direction of a *JETTA* editor, appointed by the editor-in-chief.

STYLE FOR MANUSCRIPT

1. Typeset, double or $1\frac{1}{2}$ space, use one side of sheet only (laser printer, typewritten and good quality duplication acceptable).

2. Use an informative title and provide an informative 100 to 250 word abstract at the head of the manuscript. The abstracts are printed with the articles. It should be a carefully worded description of the problem, the key ideas introduced, and the results.

3. Following the abstract, list from three to five keywords. Some typical keyword examples are: ATE, fault modeling, logic simulation, test programming.

4. Provide a double-spaced sheet listing all footnotes, beginning with affiliation of authors and continuing with numbered footnotes. Acknowledgment of financial support may be given if appropriate.

5. References should appear in a separate bibliography at the end of the paper. References should be numbered and sequenced according to the last name of the first author. A citation in the text should use the sequence number in brackets, e.g., [5]. Only the references used in the text should be listed. They should be complete and in the following style:

   Style for journal papers: Author(s), initials followed by last name for each author, paper title, *publication name*, volume, number (optional), inclusive page numbers, month and year. For example,

   J. Galiay, Y. Crouzet, and M. Vergniault, "Physical Versus Logical Fault Models in MOS LSI Circuits: Impact on Their Testability," *IEEE Trans. Computers*, vol. 29, no. 6, pp. 527-531, June 1980.

   Style for conference proceedings papers: Author(s), paper title, *conference name* preceded by *Proc.*, date, pages. For example,

   D. M. Singer, "Testability Analysis of MOS VLSI Circuits," *Proc. Int. Test Conf.*, 1984, pp. 271-277.

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   M. Abramovici, M. A. Breuer, and A. D. Friedman, *Digital Systems Testing and Testable Design*, Piscataway, New Jersey: IEEE Press, 1995.

6. Type or mark mathematical expressions exactly as they should appear in print. Journal style for letter symbols is as follows: variables, italic type; constants, roman text type; matrices and vectors, boldface type. It will be assumed that letters in displayed equations are to be set in italic type unless you mark them otherwise. All letter symbols in text discussion must be in italic or boldface, as appropriate. Indicate best breaks for equations in case they will not fit on one line.

7. Include author biographies without photograph, when submitting the final version of an "accepted" manuscript.

STYLE OF ILLUSTRATIONS

1. Originals of illustrations should be sharp, noise-free, and of good contrast. We regret that we cannot provide drafting or art service.

2. Line drawings should be in laser printer output or in India ink on paper, or board. Use $8\frac{1}{2} \times 11$-inch (22×29 cm) size sheets if possible, to simplify handling of the manuscript.

3. Each figure should be mentioned in the text and numbered consecutively using Arabic numerals. Specify the desired location of each figure in the text.

4. Number each table consecutively using Arabic numerals. Please label any material that can be typeset as a table, reserving the term "figure" for the material that has been drawn. Specify the desired location of each table in the text. Include a brief title above each table.

5. All lettering should be large enough to permit legible reduction.

6. Photographs should be glossy prints of good contrast and gradation, and or any reasonable size.

7. Number each original on the back.

8. Each figure must have a caption. provide a separate sheet listing all figure captions, in proper style, e.g., "Fig. 3. Examples of the fault coverage of random vectors in (a) combinational circuit and (b) sequential circuit."

LETTERS

The Letters section of *JETTA* publishes concise articles describing new ideas. Comments on published papers can also be submitted. Publication cycle for Letters is kept short although all submissions go through a peer/editorial review process. Professor Kewal K. Saluja (saluja@engr.wisc.edu) is the editor for *JETTA* Letters.

Submissions to the Letters section should be no longer than six pages including double or 1.5 line-spaced text, figures, tables and references. Authors should follow the same manuscript style as specified for regular papers and send *three* (3) copies to Ms. Bonny L. Blaisdell at the *JETTA* Editorial Office, clearly indicating that the submission is for the Letters section. Letters are printed without author biographies.

ELECTRONIC DELIVERY

After a paper has been reviewed, revised if necessary, and accepted, the author will be requested to submit an electronic version (ev).

PROOFING

Page proofs for articles to be included in a journal issue will be sent to the contact author for proofing, unless otherwise informed. The proofread copy should be received back by the Publisher within 72 hours.

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