Proposal to host VLSI Design 2003 conference at New Delhi

Theme:
Mixed Signal Systems-on-a-Chip

Proposed Dates:
Jan 4, 2003 (Saturday) to Jan 8, 2003 (Wednesday)

Possible Venues:
1. India Habitat Center
2. Hotel Taj Palace
3. Hotel Grand Hyatt

- Delhi is easily accessible for international as well as domestic participants.
- There is a growing local industry (Motorola, STMicroelectronics, Ikos, Delsoft, Interra, NIIT/Mentor Graphics, Tality, Virage Logic, Cadence, SCL, CEERI, etc.) and several premier engineering colleges (IIT-D, IIT-K, DIT, DCE, REC-Kurukshetra, PEC, BITS-Pilani, etc.) in and around Delhi to ensure good participation with regards to conference attendance and sponsorship/financial support.
- A half-day local sight seeing tour, or a day excursion to Agra can be planned for out of town attendees.

Target Attendance:
Paid registrations: 700
Fellowships: 300

Proposed Conference Committee:
Steering Committee Chair: Vishwani Agarwal, Bell Labs, Lucent, Murray Hill, NJ, USA

General Chair:	Jaswinder Ahuja, Cadence Design Systems, Noida, India
Program Chairs:	M. Balakrishnan, IIT-Delhi, Delhi, India Srimat T. Chakradhar, NEC, Princeton, NJ, USA
Tutorial Chairs:	TBD TBD
Design Contest Chairs:	Avinash Gautam, Texas Instruments, India Sushil Gupta, Motorola, Noida, India
Publicity Chairs:	C. P. Ravikumar, IIT-Delhi, Delhi, India (TBC) Vamsi Krishna Srikantam, HP Labs, Palo Alto, CA, USA (TBC) Bernard Courtois from Europe (TBC) TBD from Asia/Japan
Publications Chair:	TBD from USA
Industry Forum Chairs:	Apurva Kalia, Cadence Design Systems, Noida, India TBD from USA
Exhibits & Sponsorships Chairs:	Rajeev Sehgal , Ikos, Noida, India TBD from Bangalore
Fellowship Chair:	M. Jagadeesh Kumar, IIT-Delhi, Delhi, India (TBC)
Local Arrangements & Finance:	Sanjay Mittal, Delsoft, Noida, India Jyotirmoy Daw, Ikos, Noida, India